

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

MODULAR DLL ARCHITECTURE FOR GENERATING
MULTIPLE TIMINGS

Application Number :

10/707067

Confirmation Number:

First Named Applicant:

Darren Anand

Attorney Docket Number:

BUR920030161US1

Art Unit:

2816

Examiner:

Search string:

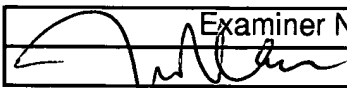
(6147527 or 6310822 or 6374360 or 0064082 or 6424592),pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
n	1	6147527	2000-11-14	Lee et al		327	141
n	2	6310822	2001-10-30	Shen		365	233
n	3	6374360	2002-04-16	Keeth et al		713	460
n	4	0064082	2002-05-30 4/67	Maruyama Dick		296	65.05
n	5	6424592	2002-07-23	Maruyama		365	233

Signature

	Examiner Name	Date
		10/31/04